



"Memory Device Having A Programmable Register"

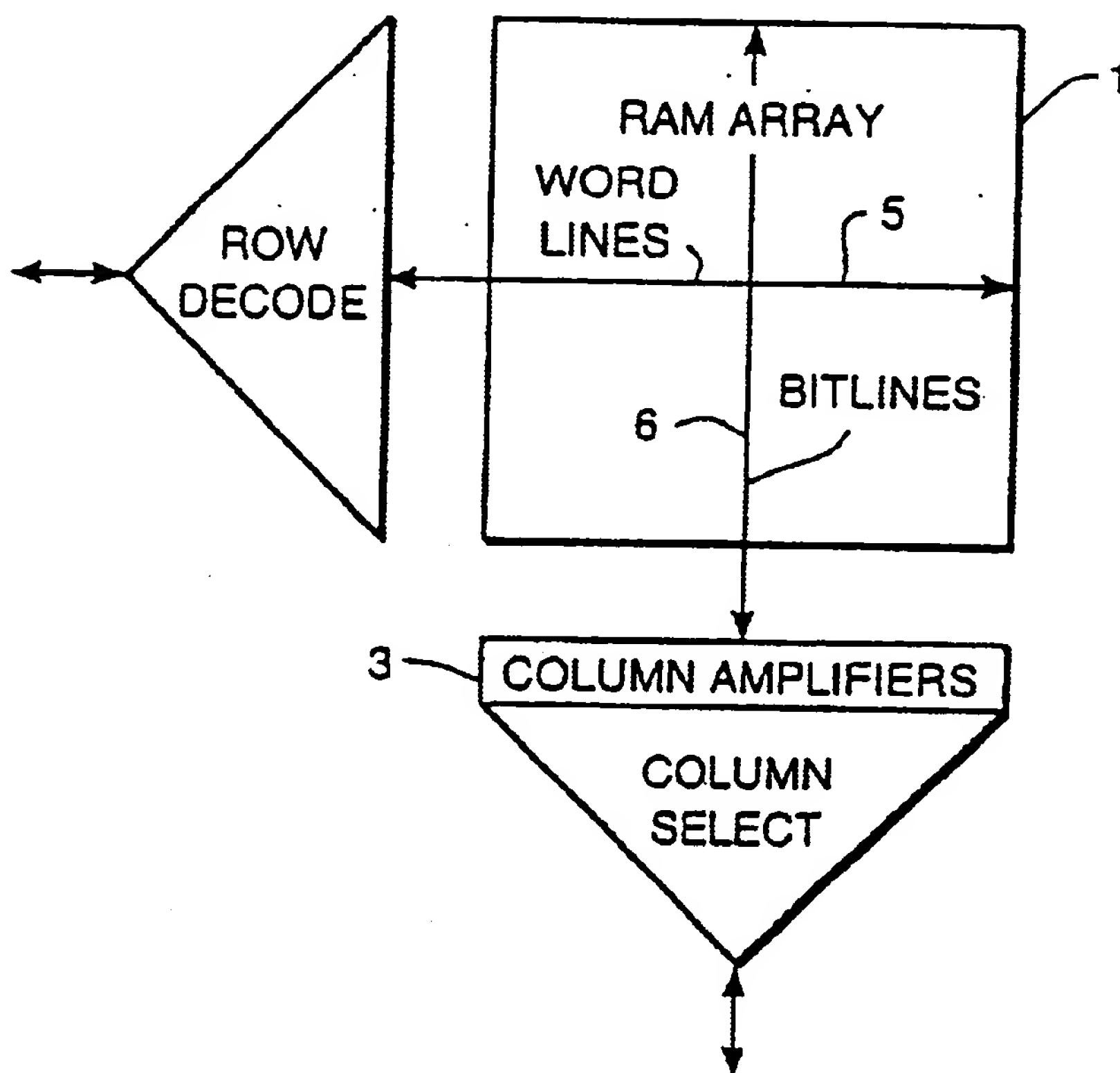
Applicant: Farnwald, et al.

Application No. 09/801,151

Attorney Docket No. 57941.000026

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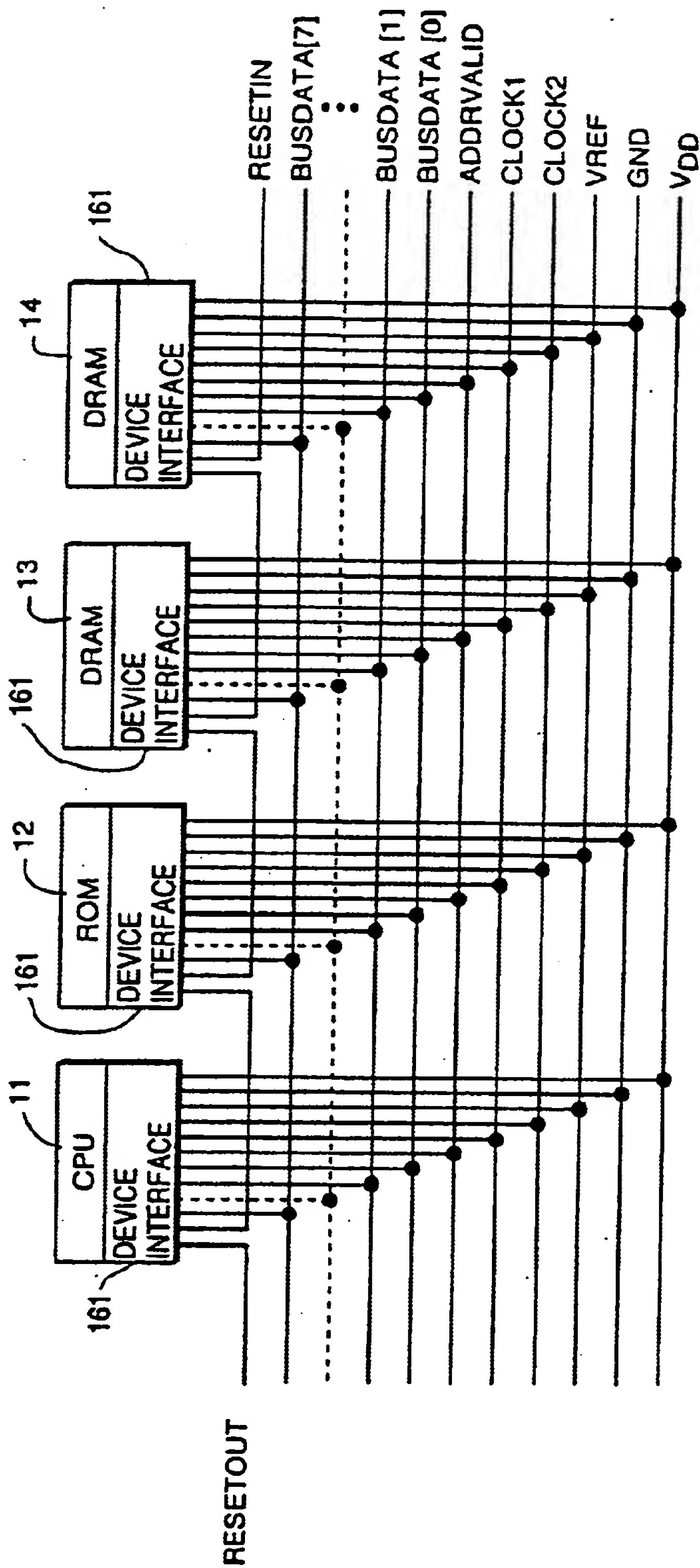
**FIG 1**



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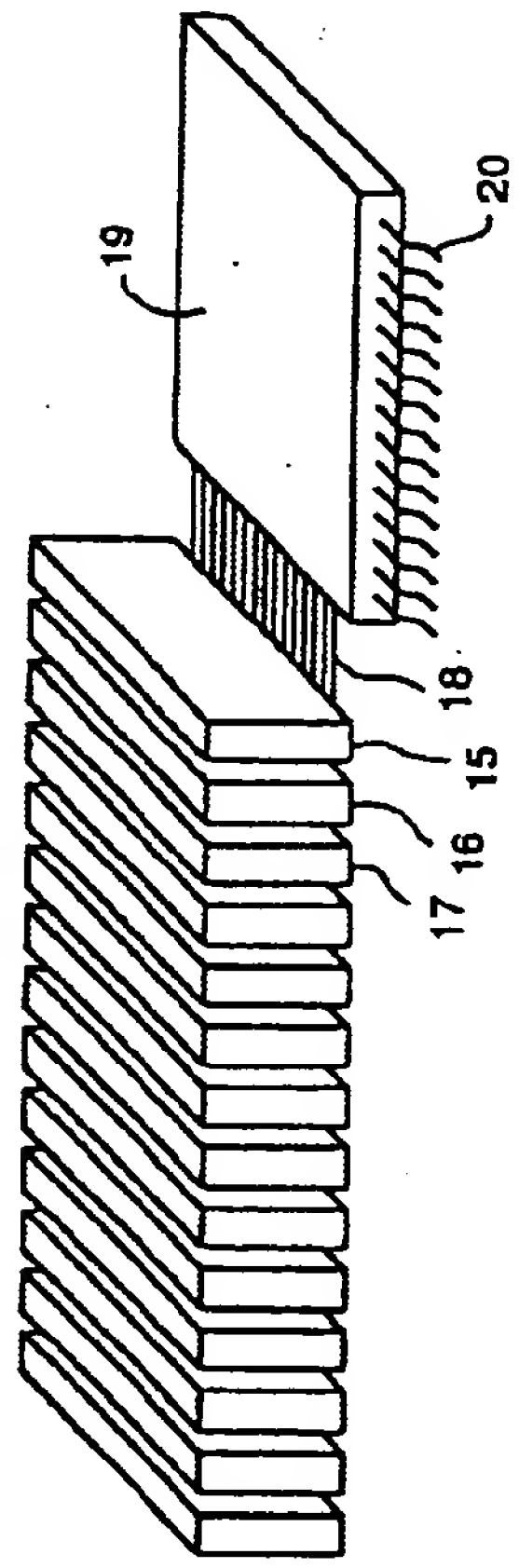
**FIG. 2**



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FIG 3



REGULAR ACCESS

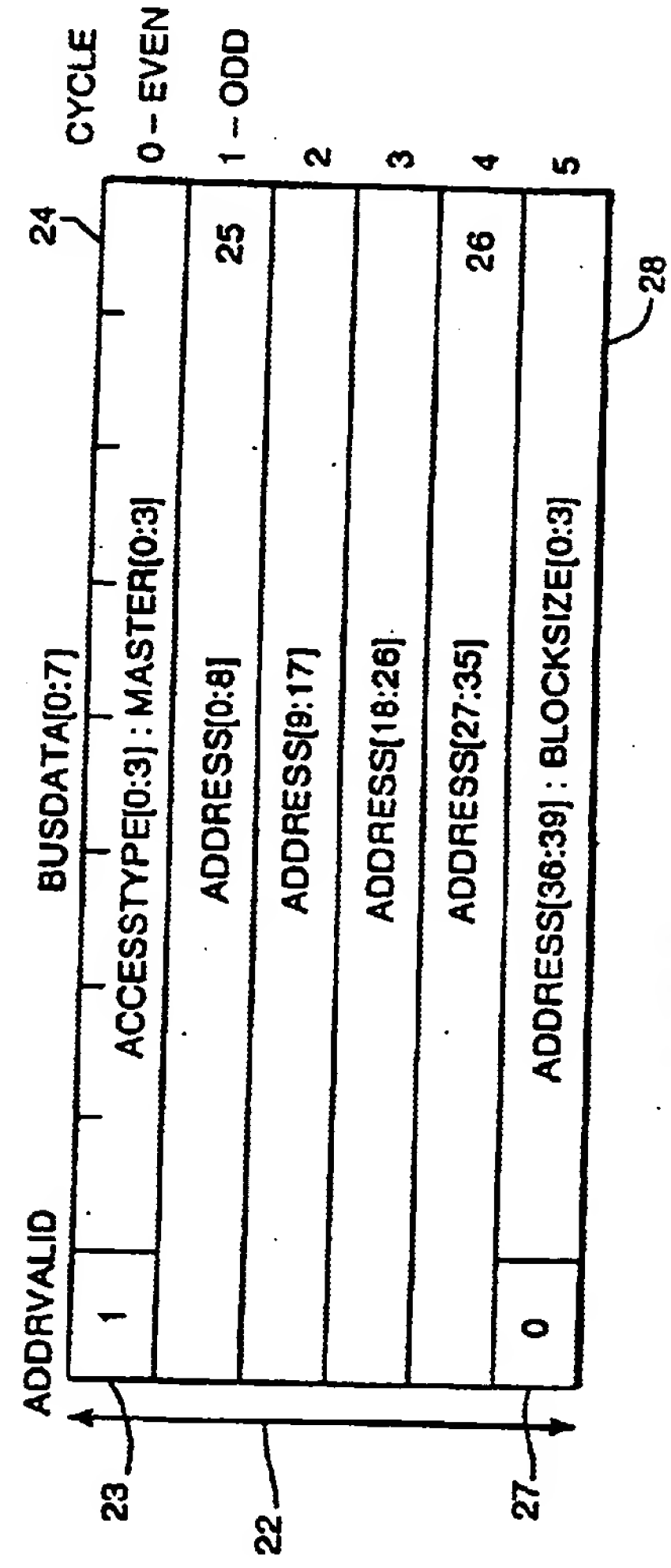


FIG 4



REJECT (NACK) CONTROL PACKET

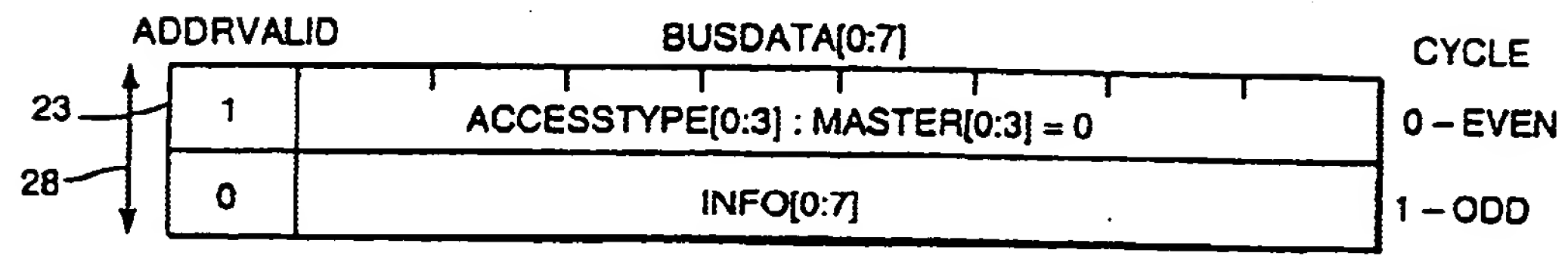


FIG 5

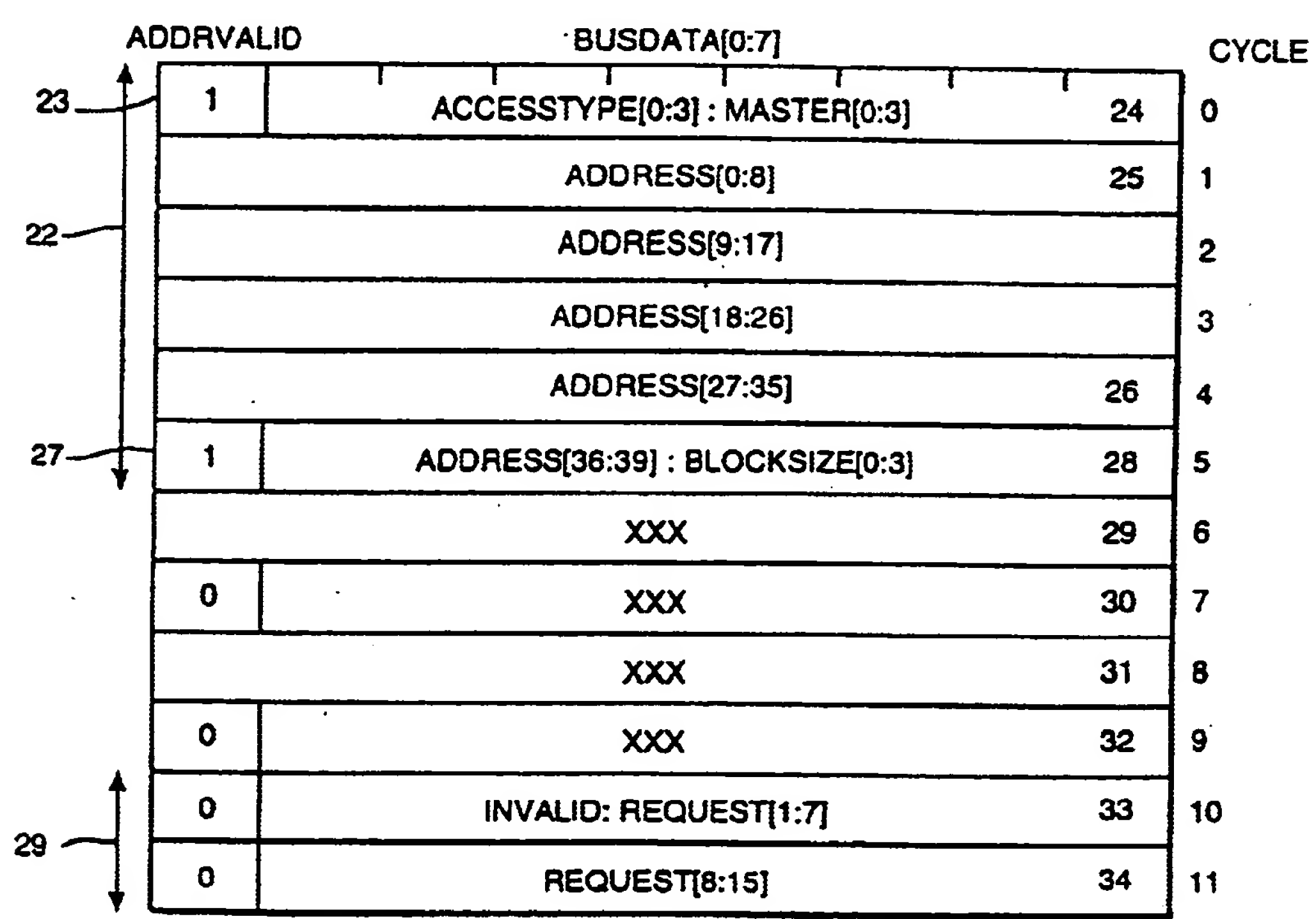
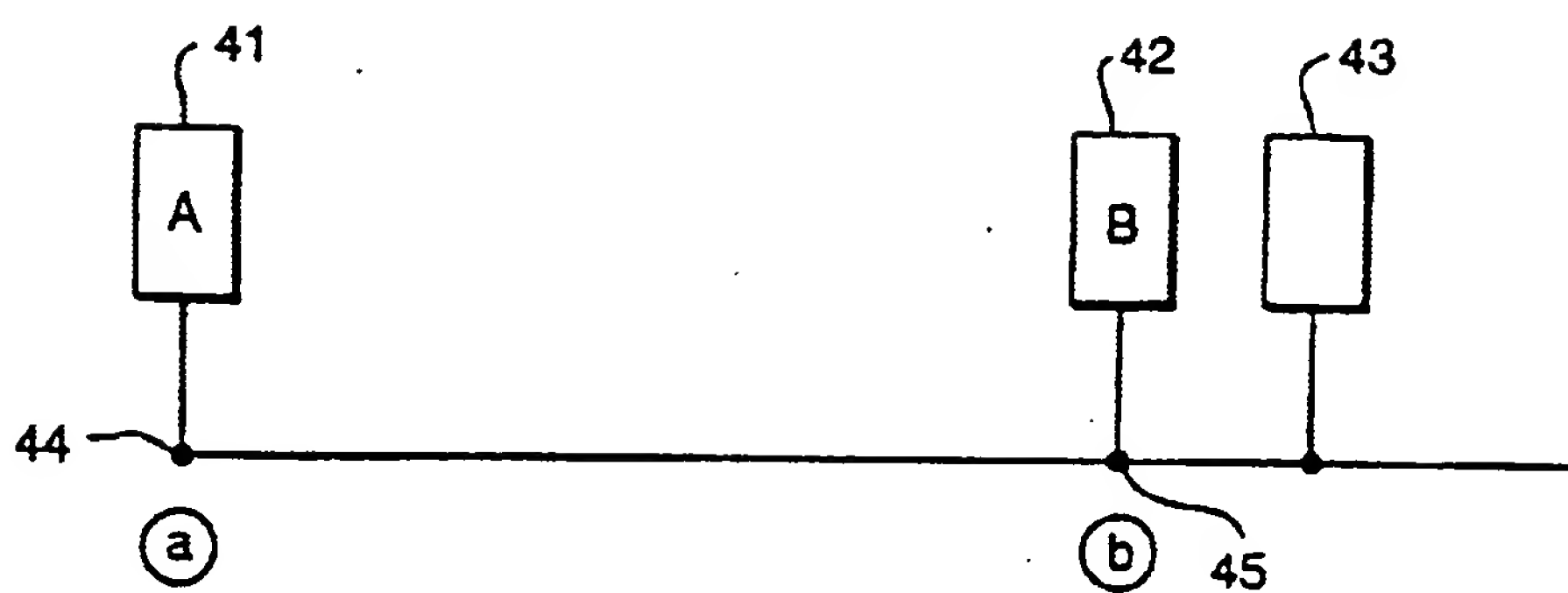


FIG 6



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**FIG\_7A**



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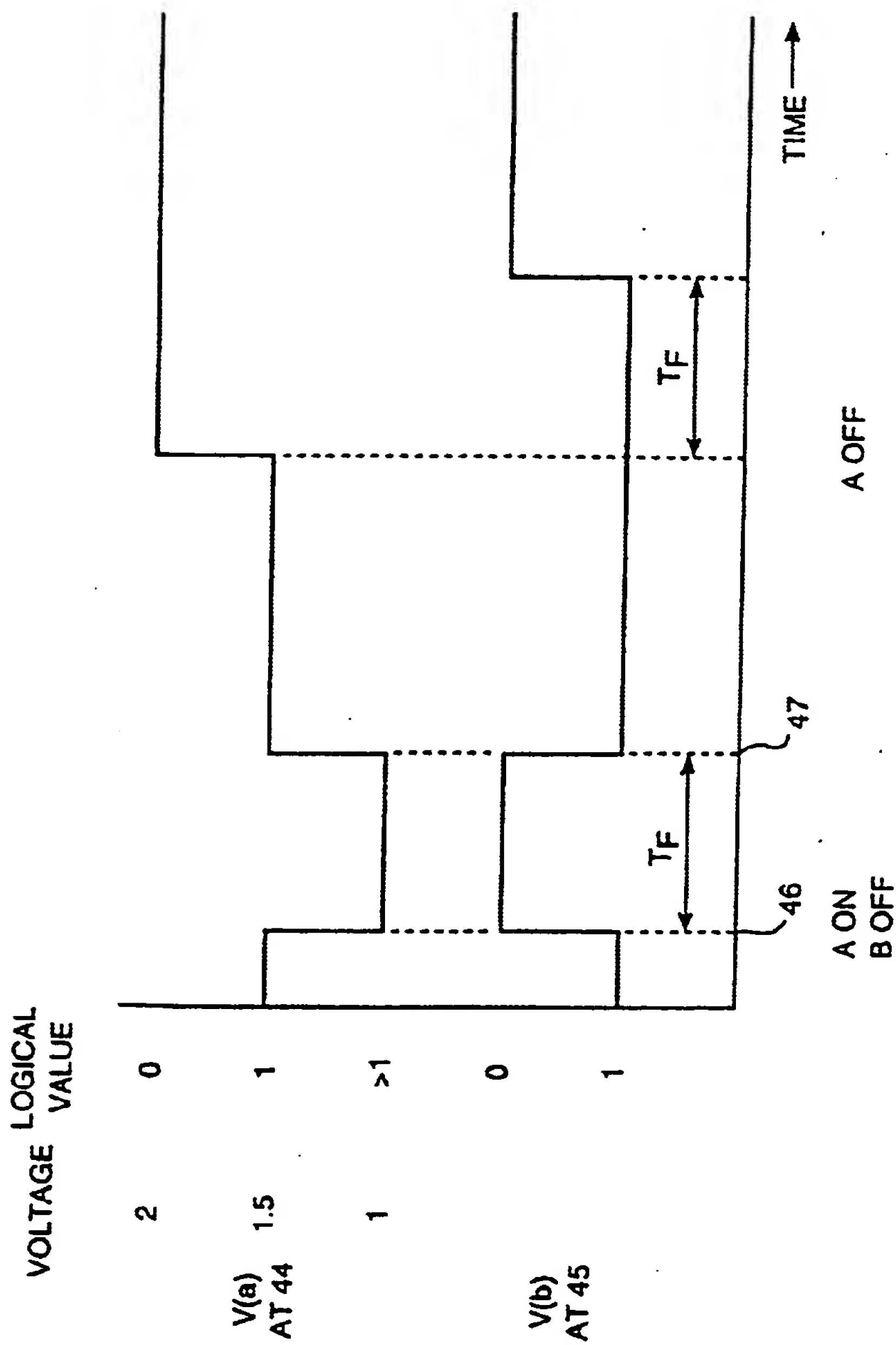
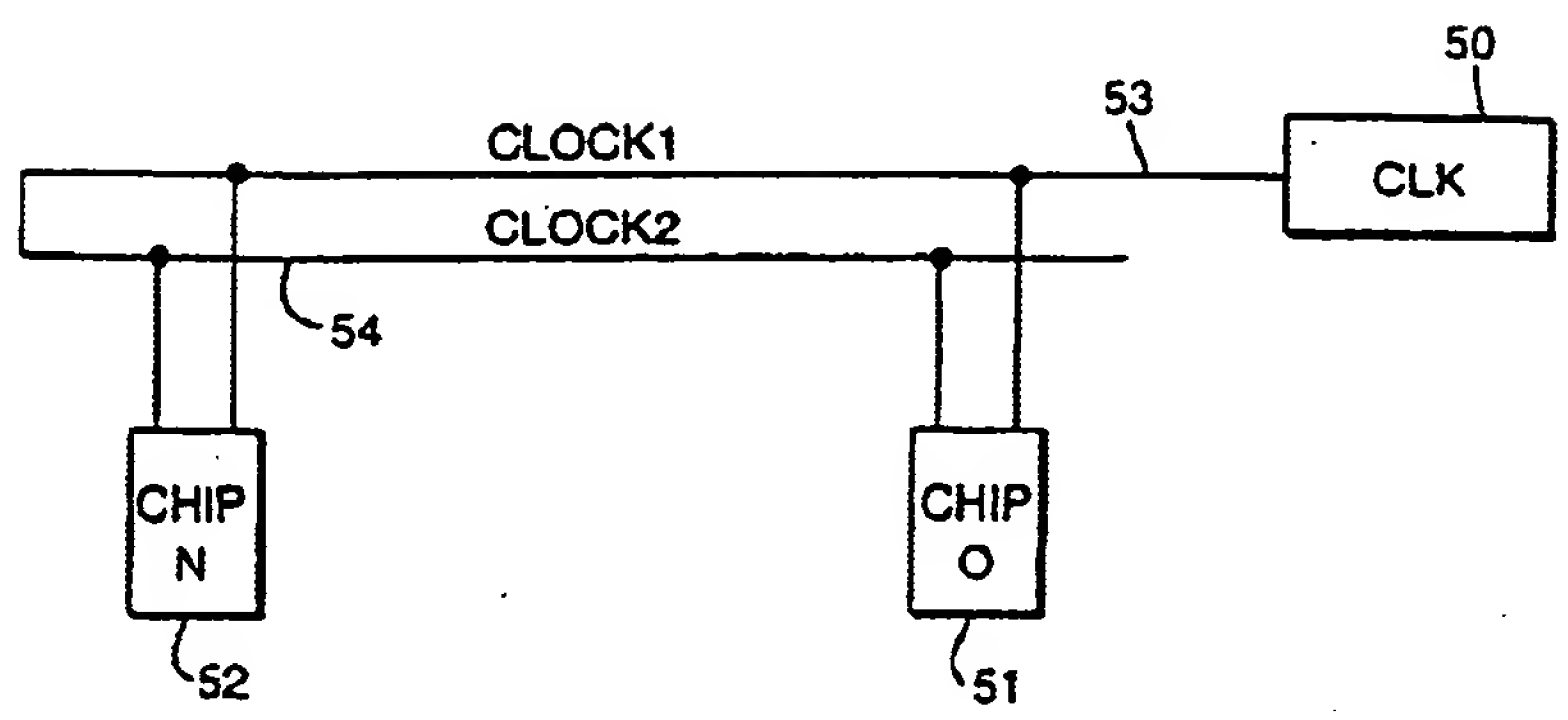


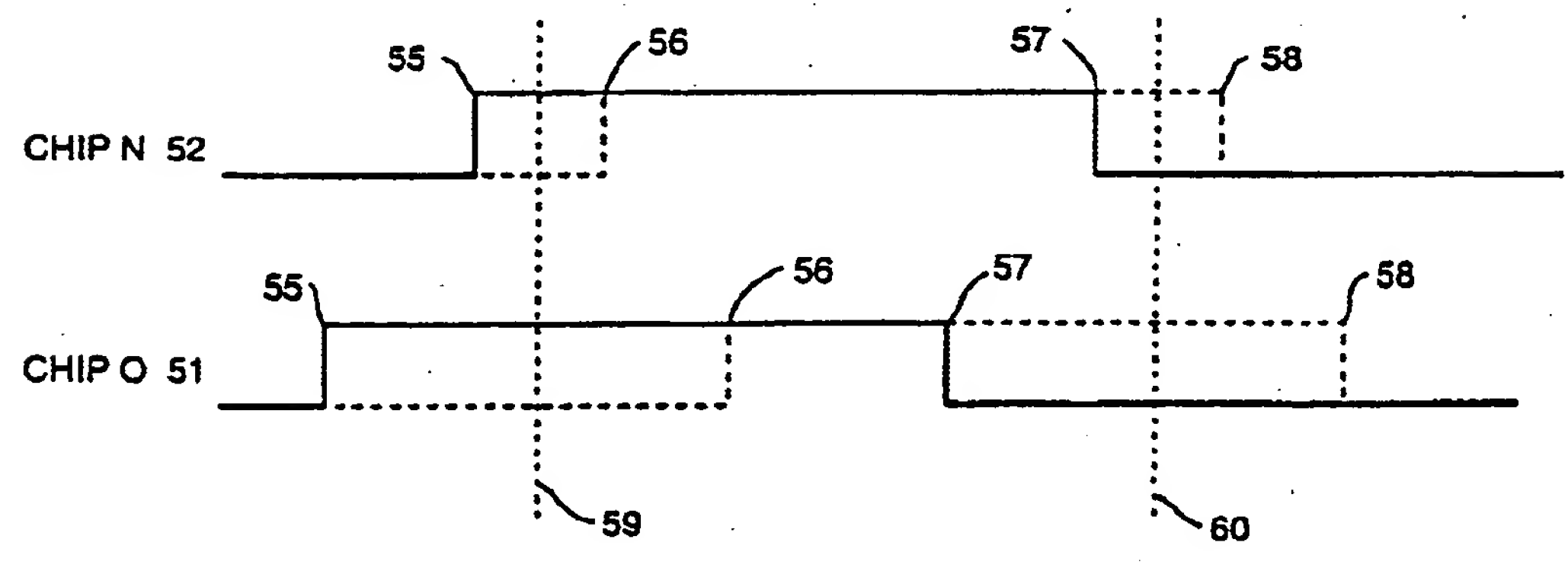
FIG. 7B

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**FIG 8A**

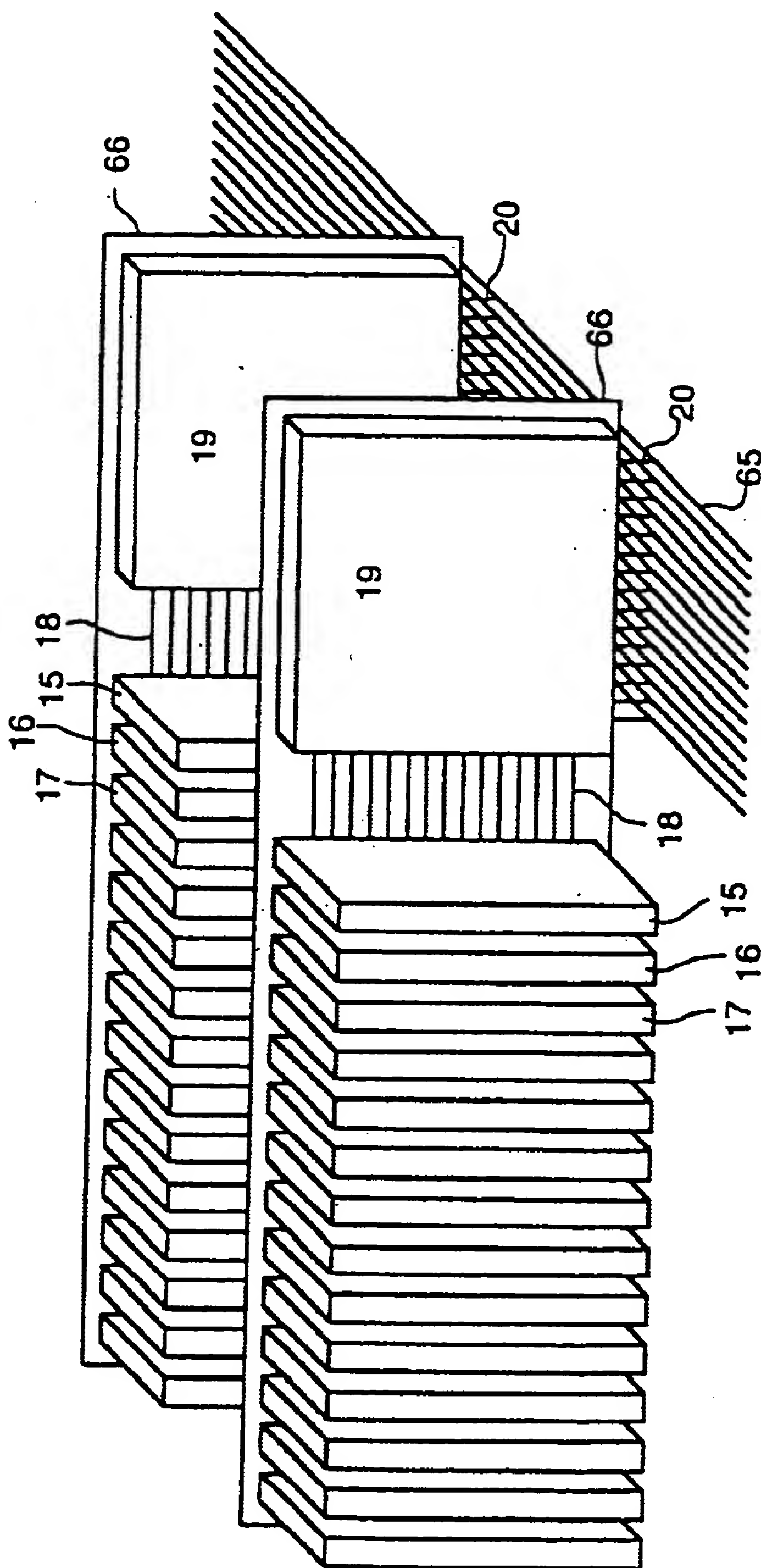


**FIG 8B**

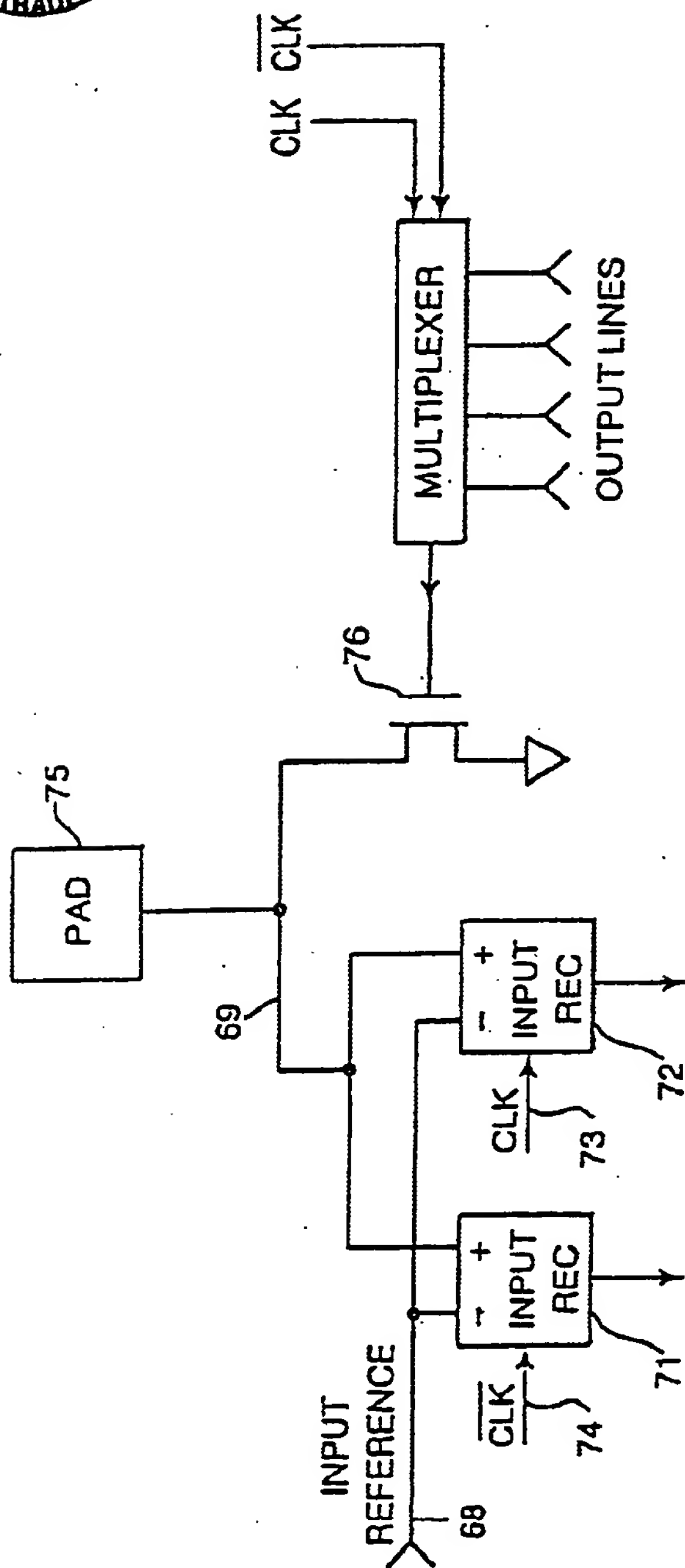


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**FIG 9**





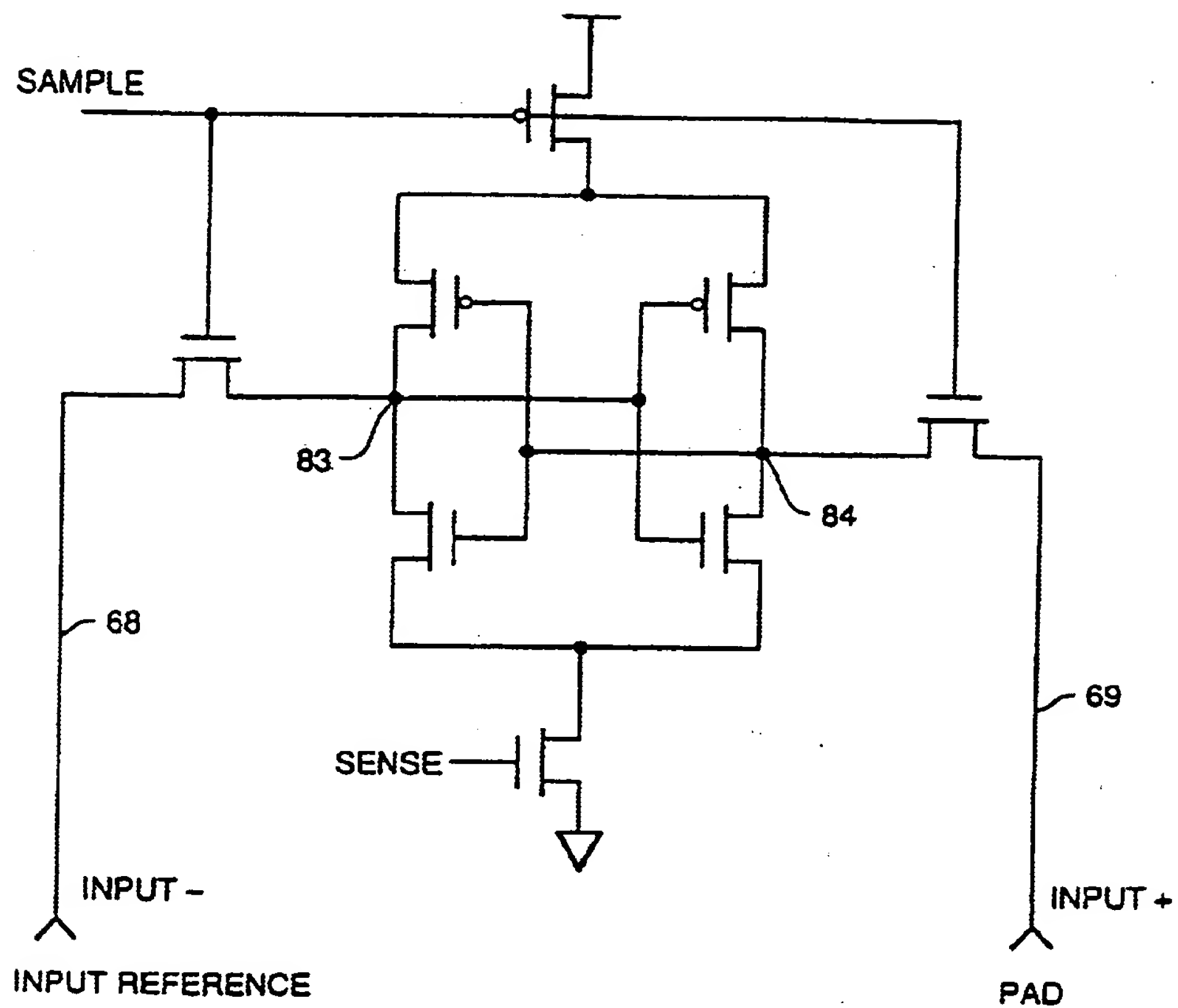


**FIG. 10**

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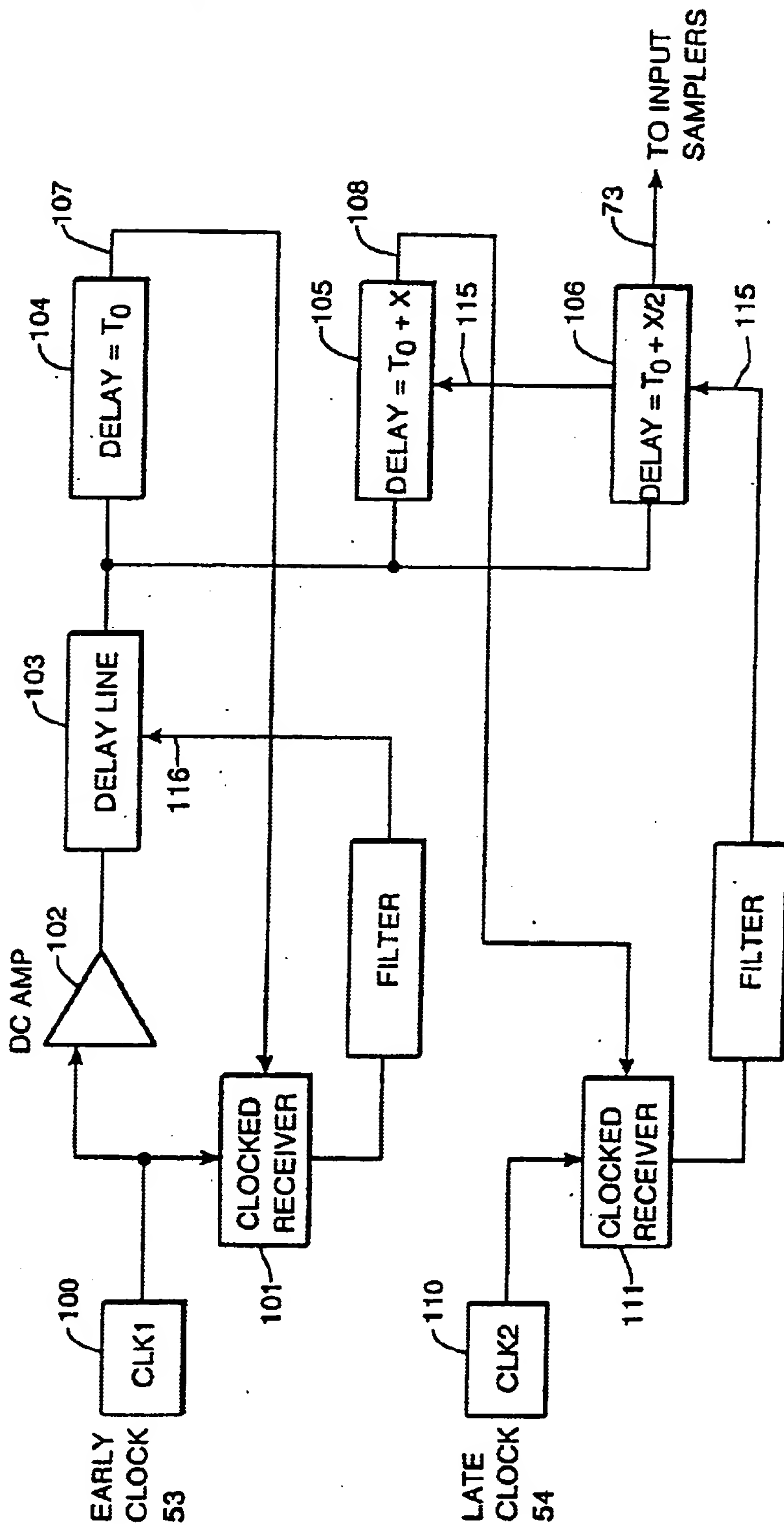
**FIG. 11**



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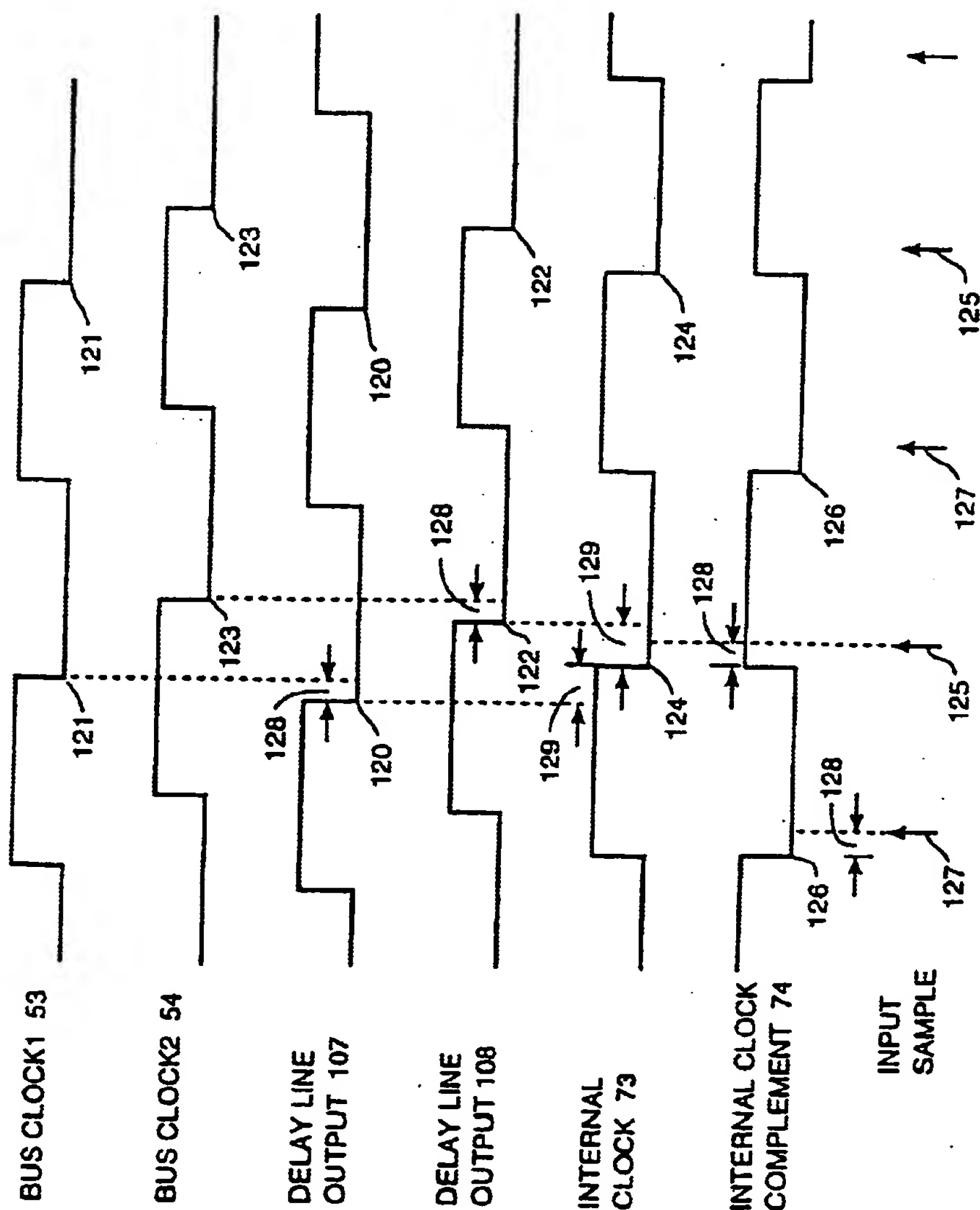
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 Applicant: Samwald, et al.  
 Application No. 09/801,151  
 Attorney Docket No. 57941.000026

**FIG. 12**



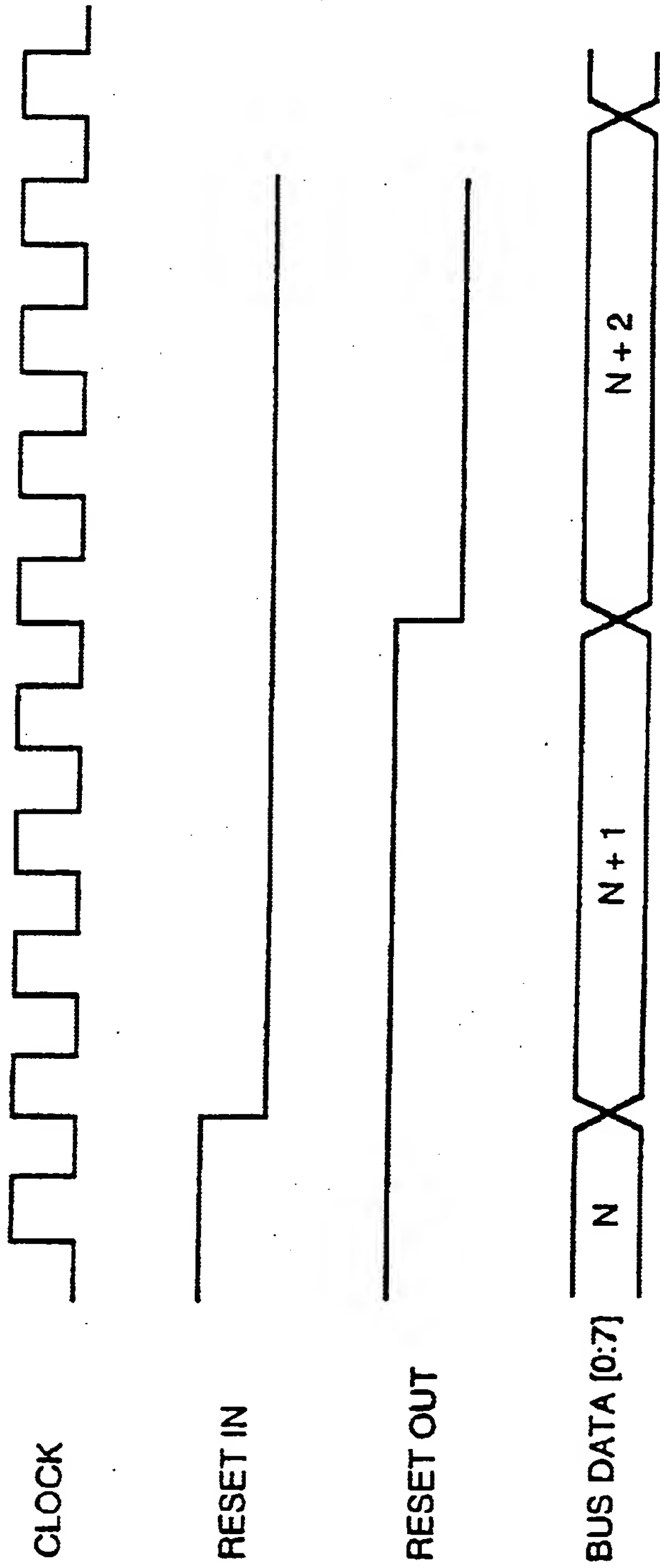


**FIG. 13**



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**FIG. 14**

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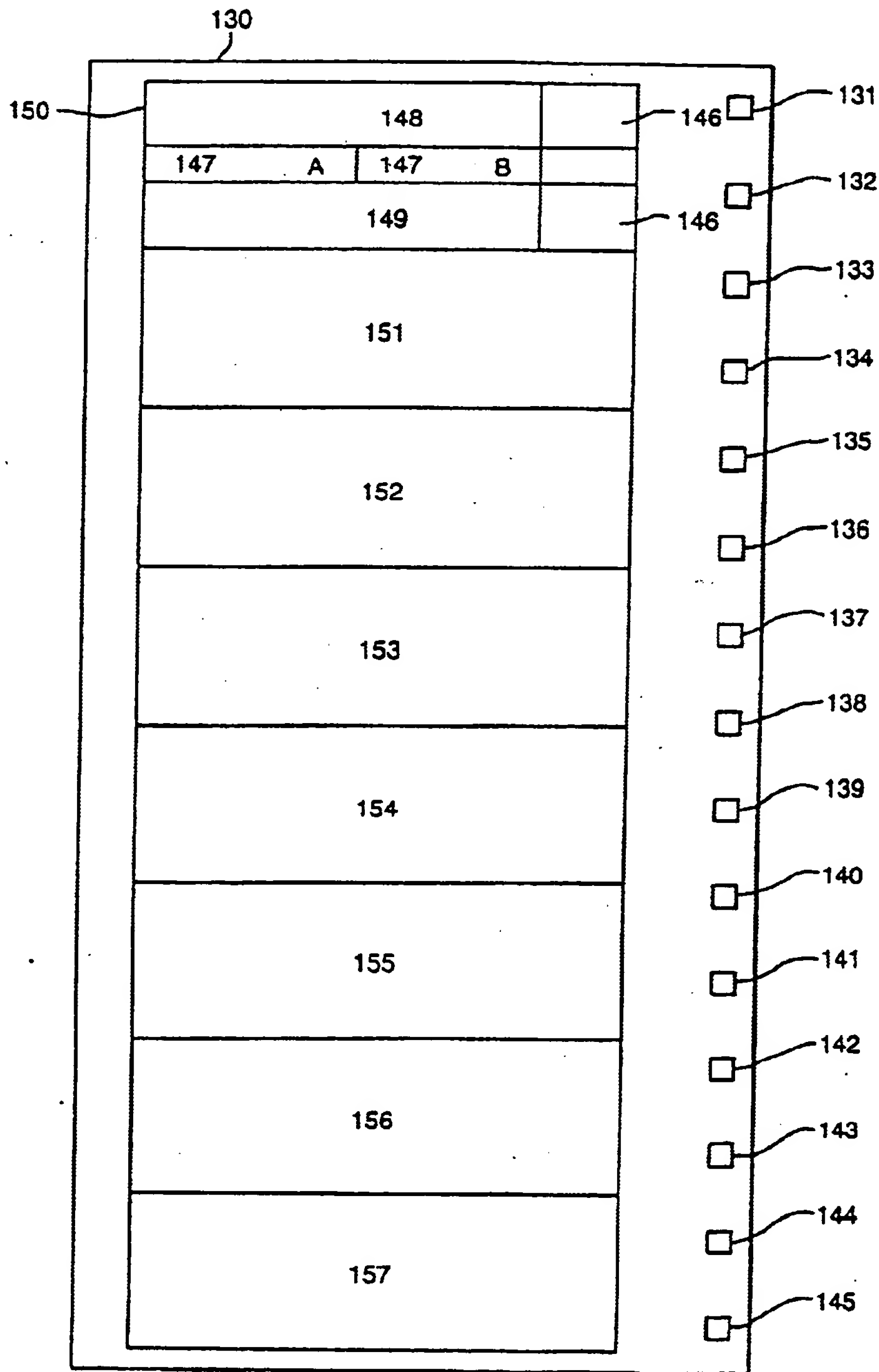
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Applicant: Farnwald, et al.

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# FIG. 15



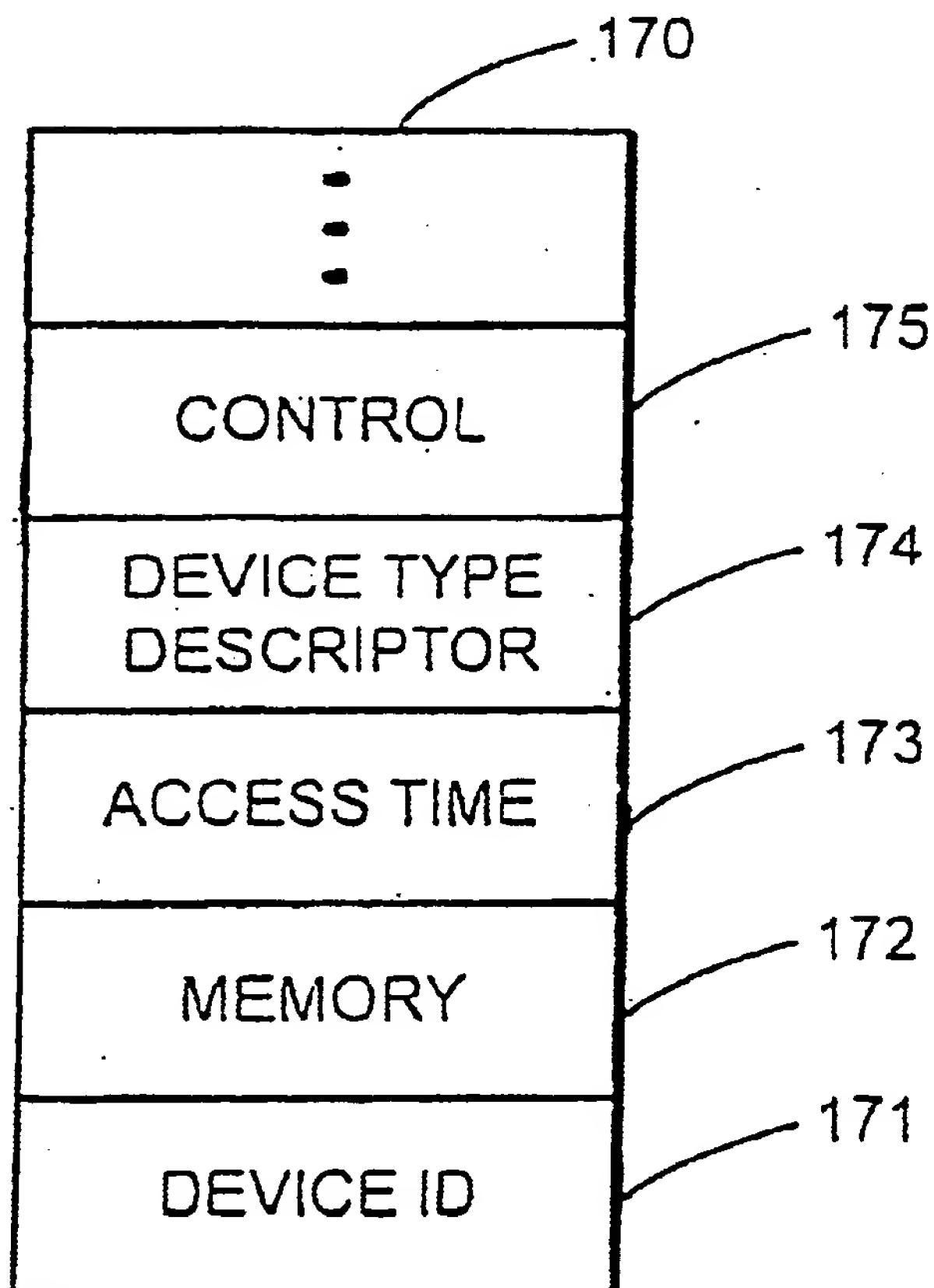


FIG. 16